



# StellarStudio SR5X

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## StellarStudio SR5X User Guide

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# Contents

1 Creation of a SR5x project .....4

    1.1 .....4

2 Disclaimer .....10



# List of figures

Figure 1. Create a project .....4

Figure 2. Select an E-SDK version .....5

Figure 3. Put a name .....6

Figure 4. Open make file .....7

Figure 5. Choose a board .....8

Figure 6. Compile .....8

Figure 7. Result .....9

# 1 Creation of a SR5x project

## 1.1

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Click on the icon or press <CTRL><N>

**Figure 1: Create a project**

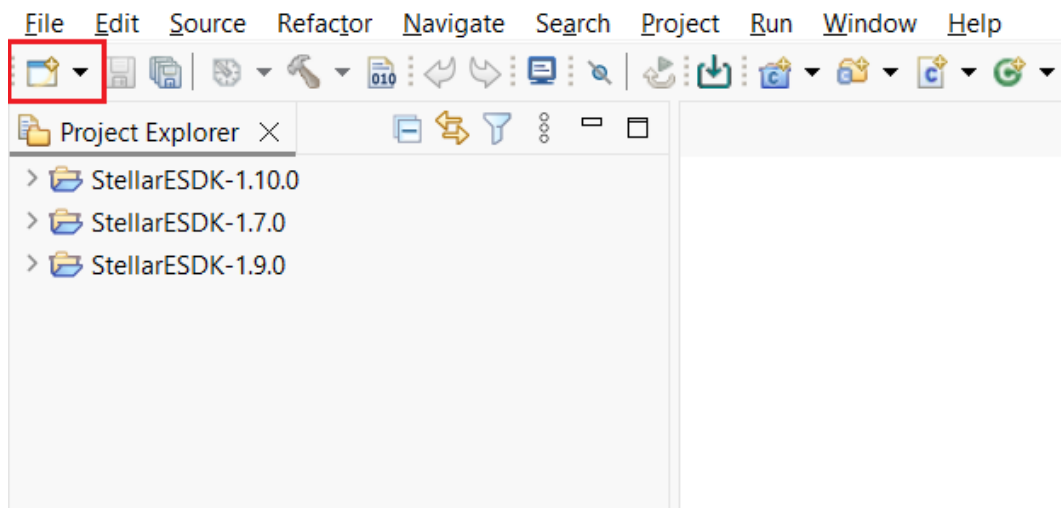


Figure 2: Select an E-SDK version

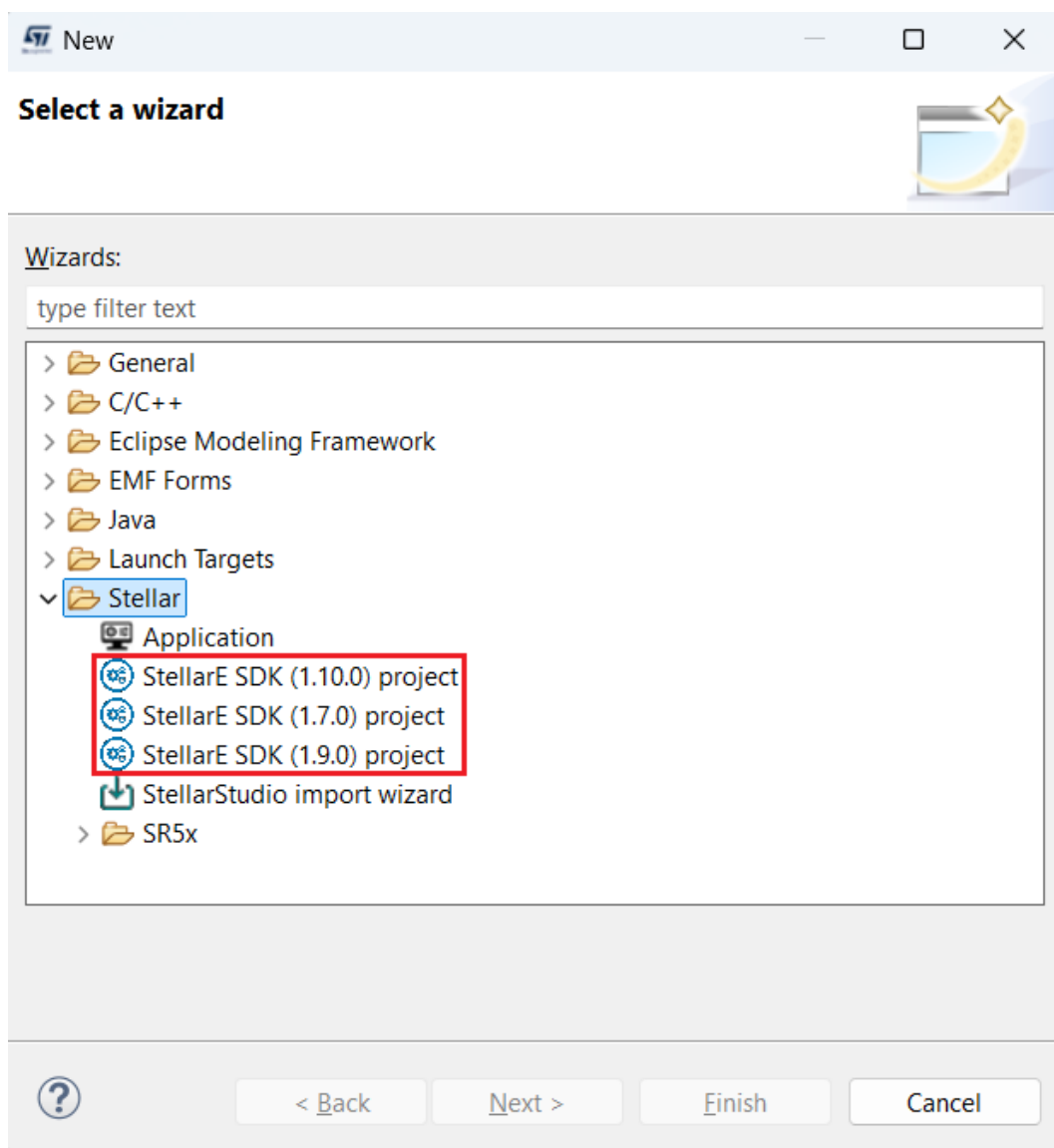
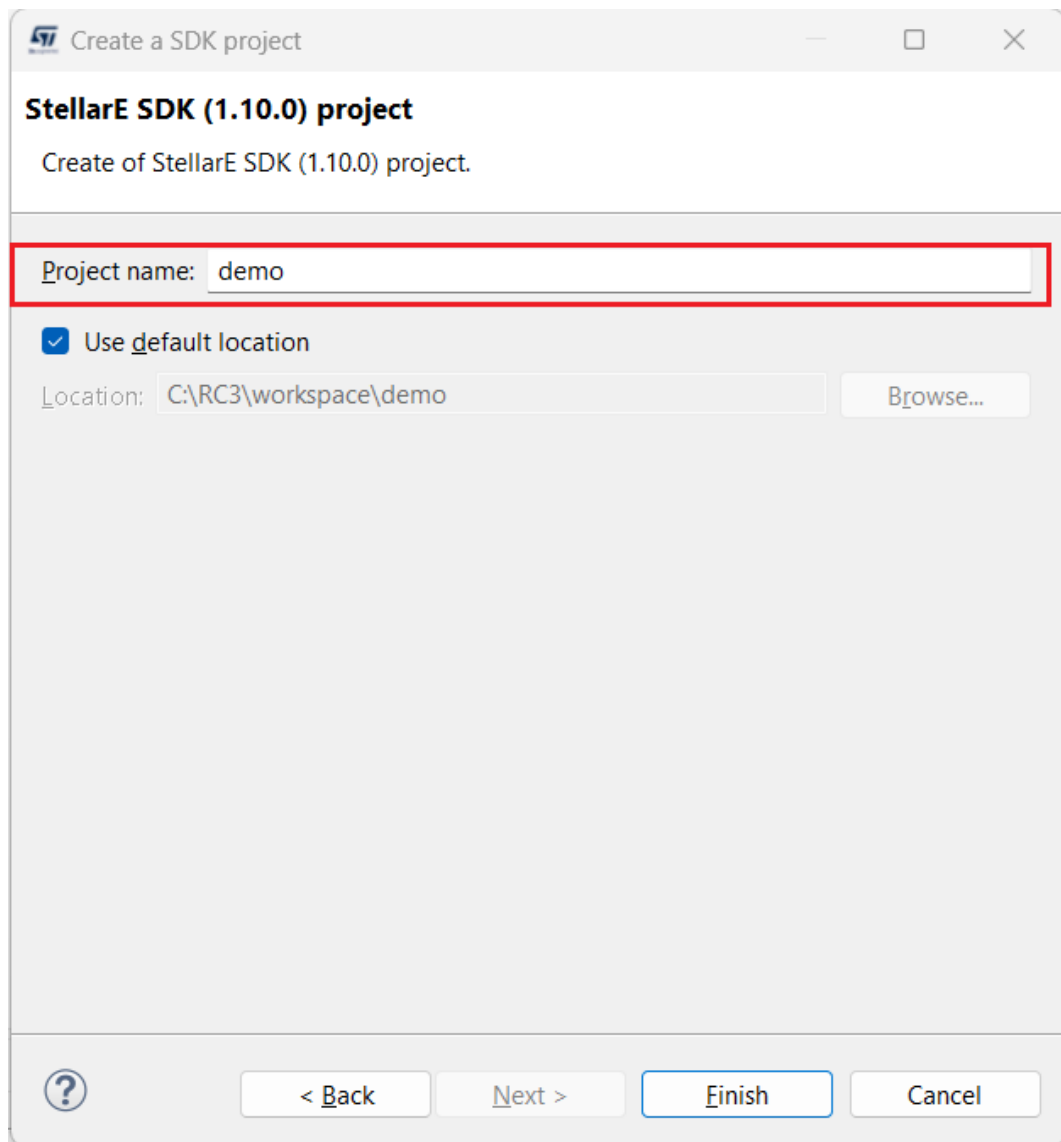


Figure 3: Put a name



ST Create a SDK project

**StellarE SDK (1.10.0) project**

Create of StellarE SDK (1.10.0) project.

Project name: demo

☒ Use default location

Location: C:\RC3\workspace\demo Browse...

? < Back Next > Finish Cancel

Figure 4: Open make file

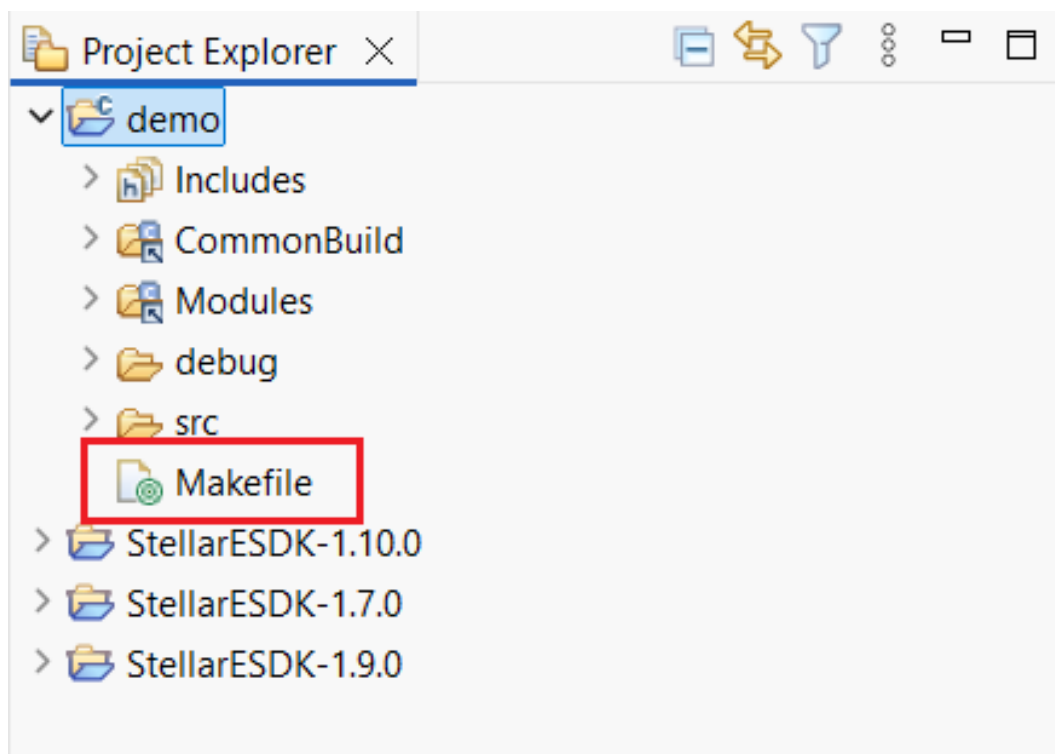


Figure 5: Choose a board

```

#####
# Project makefile.
#####

# 'Stellar SDK' project values
PROJECTNAME := demo
SDKID := StellarESDK-1.10.0

# Please Update it if you move your workspace
STELLAR_E_SDK_RELATIVE_PATH := ../../SDKS/$(SDKID)
TEST_ROOT_DIR := $(STELLAR_E_SDK_RELATIVE_PATH)
PROJECT_COMMON_DIR := $(STELLAR_E_SDK_RELATIVE_PATH)/Projects/SDKTests/CommonBuild

#####
# Define device (sr5e1)
# Define board -
# sr5e1 ==> (evbe7000p/evbe7000s/evbe3000p/evbe7000e/evbe3000e/evbe3000d)
# WARNING : your board should match with your device
#####

CONFIG_DEVICE ?= sr5e1
CONFIG_BOARD ?= evbe7000p
# ram or nvm
CONFIG_TARGET_MEMORY ?= ram

include $(PROJECT_COMMON_DIR)/make/test_defs.mk

ifeq ($(wildcard $(STELLAR_E_SDK_BUILD_SYSTEM_DIR)/StellarESDK.mk),)
$(error $(STELLAR_E_SDK_BUILD_SYSTEM_DIR)/StellarESDK.mk does not exist)
$(error Install the $(SDKID) or Correct STELLAR_E_SDK_RELATIVE_PATH)
endif

#####
# Project builds
#####
BUILD_OS_OSAL := 1

#####
# Add project files

```

Figure 6: Compile

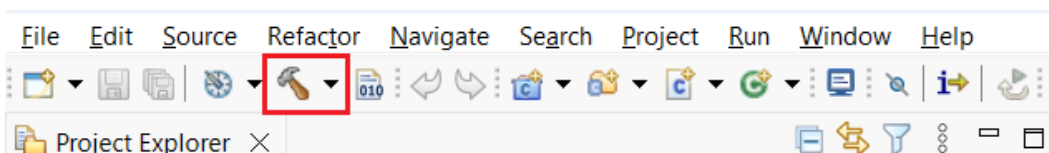
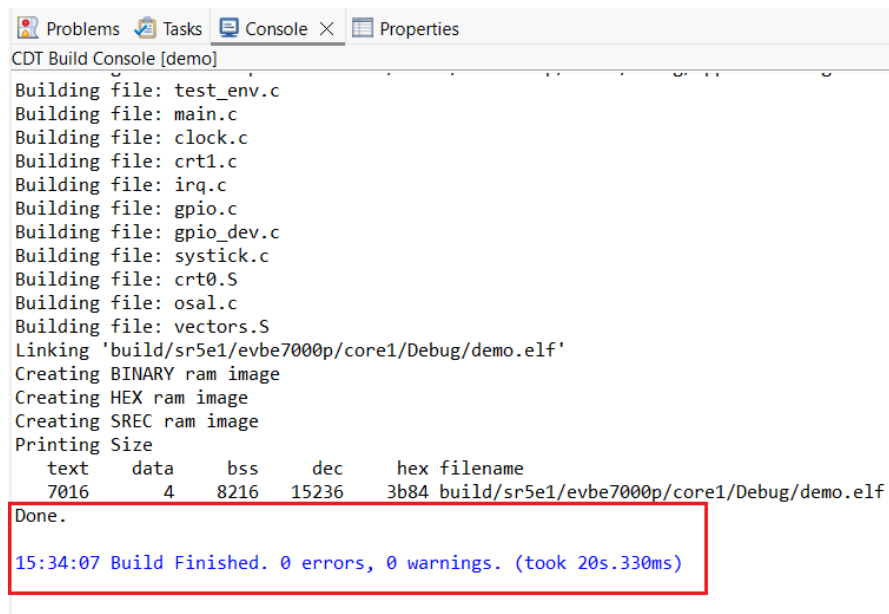




Figure 7: Result



```
CDT Build Console [demo]
Building file: test_env.c
Building file: main.c
Building file: clock.c
Building file: crt1.c
Building file: irq.c
Building file: gpio.c
Building file: gpio_dev.c
Building file: systick.c
Building file: crt0.S
Building file: osal.c
Building file: vectors.S
Linking 'build/sr5e1/evbe7000p/core1/Debug/demo.elf'
Creating BINARY ram image
Creating HEX ram image
Creating SREC ram image
Printing Size
  text    data    bss     dec      hex filename
  7016      4    8216   15236   3b84 build/sr5e1/evbe7000p/core1/Debug/demo.elf
Done.

15:34:07 Build Finished. 0 errors, 0 warnings. (took 20s.330ms)
```

Your SR5x project is ready !

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